



**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

ITOH et al.

Art Unit: 2819

Application No.: 09/735,555

Examiner: D. Chang

Filed: December 14, 2000

Attorney Dkt. No.: 108075-00022

For: OUTPUT BUFFER CIRCUIT AND CONTROL METHOD THEREFOR

**AMENDMENT UNDER 37CFR §1.111**

Commissioner for Patents  
Washington, D.C. 20231

Date: April 11, 2002

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TECHNOLOGY CENTER 2800

Sir:

This paper responds to the Office Action dated December 11, 2001, the time period for response having been extended from March 11, 2002 until April 11, 2002 by the enclosed Petition for Extension of Time. In the prosecution of the application identified above, please consider the following amendments:

**IN THE CLAIMS:**

Please amend claims 2 and 5 as follows:

2. (Amended) A method of controlling an output buffer circuit comprising first and second drive circuits, the first drive circuit including a first output transistor connected between a first power supply and an output terminal of the output buffer circuit and a second output transistor connected between a second power supply and the output terminal, wherein the first and second output transistors generate an output